

WHAT IS CLAIMED IS:

1. A disk array device comprising:

a channel adapter that controls data transmission and reception with a high-order device;

a storage device that stores data;

a storage device control board to which the storage device is connected;

a disk adapter that is connected to the storage device via the storage device control board and controls data transmission and reception with the storage device; and

a management unit that is respectively connected to the disk adapter and the channel adapter, wherein

the storage device control board includes

a connection circuit that is connected to the storage device, and

switch circuits that are respectively disposed at an input side and an output side of the connection circuit and are switchable between a connected mode where they are connected to another adjacent storage device control board and an independent mode where they are separated from the other adjacent storage device control board, and

the switch circuits are switchable between the connected mode and the independent mode by an output signal from the management unit.

2. The disk array device of claim 1, wherein the storage device control board and the other storage device control board are respectively mounted on a same attachment-use board.

3. The disk array device of claim 1, wherein
in a case where the switch circuits are in the connected mode, the storage device control board and the other storage device control board are respectively connected to the same disk adapter, and

in a case where the switch circuits are in the independent mode, the storage device control board and the other storage device control board are connected to respectively different disk adapters.

4. The disk array device of claim 1, wherein the storage device includes a first port and a second port, with the first port and the second port being connected to respectively different storage device control boards and the storage device control boards being connected to respectively different disk adapters.

5. The disk array device of claim 1, wherein the connection circuit is configured by any of a port bypass circuit and a fibre channel switch.

6. The disk array device of claim 4, wherein respectively different colors are associated with input-side connectors and output-side connectors with which the disk adapter and the storage device control board are disposed, and

respectively different colors are associated with signal lines associated with the first port and signal lines associated with the second port of signal lines connecting the respective connectors to each other.

7. A method of changing the configuration of a disk array device including

a channel adapter that controls data transmission and reception with a high-order device,

plural storage devices that respectively store data,

a disk adapter that controls data transmission and reception with the storage devices, and

a management unit that is respectively connected to the disk adapter and the channel adapter, wherein

in a case where a connected mode instruction is issued from the management unit, the storage devices are together connected to each other and to the same disk adapter, and

in a case where an independent mode instruction is issued from the management unit, the storage devices are divided into plural storage device groups and the storage device groups are respectively connected to different disk adapters.

8. A storage device comprising:
an attachment-use board; and
plural storage device control boards that are
respectively disposed on the attachment-use board and connected
to the storage device, wherein
the storage device control boards include
a connection circuit that is connected to the storage
device,
a first switch circuit that is disposed at an input side
of the connection circuit, and
a second switch circuit that is disposed at an output
side of the connection circuit, and
in a case where a connected mode signal is inputted from
the outside, the storage device control boards become usable
with being connected to each other via the switch circuits,
and in a case where an independent mode signal is inputted from
the outside, the storage device control boards become usable
with being respectively separated.